

DECLARATION FOR PATENT APPLICATION (WITH POWER OF ATTORNEY)

As an inventor named below or on any attached continuation page, I hereby declare that:
My residence, post office address and citizenship are as stated next to my name.

I believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled METHOD, CIRCUIT AND SYSTEM FOR DETERMINING BURN-IN RELIABILITY FROM WAFER LEVEL BURN-IN, the specification of which (check one):

- ☒ is attached hereto.
☐ as filed on _____ as United States application serial no. _____ and was amended on _____.
☐ as filed on _____ as PCT international application no. _____ and was amended under PCT Article 19 on _____.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose to the U.S. Patent and Trademark Office all information known to me to be material to the patentability of the subject matter claimed in this application, as "materiality" is defined in Title 37, Code of Federal Regulations § 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, § 119(a)-(d) or § 365(b) of any foreign application(s) for patent or inventor's certificate or § 365(a) of any PCT international application(s) designating at least one country other than the United States of America listed below and on any attached continuation page and have also identified below and on any attached continuation page any foreign application for patent or inventor's certificate or any PCT international application(s) designating at least one country other than the United States of America having a filing date before that of the application(s) on which priority is claimed.

Prior foreign/PCT application(s):

			Priority	Claimed
_____	_____	_____	_____	_____
(number)	(country)	(day/month/year filed)	Yes	No
_____	_____	_____	_____	_____
(number)	(country)	(day/month/year filed)	Yes	No

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) or § 365(c) of PCT international application(s) designating the United States of America listed below and on any attached continuation page and, insofar as the subject matter of each of the claims of this application is not disclosed in any such prior application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose to the U.S. Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations § 1.56 which became available

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Invention Title: METHOD, CIRCUIT AND SYSTEM FOR DETERMINING BURN-IN RELIABILITY FROM
WAFER LEVEL BURN-IN

between the filing date of such prior application and the national or PCT international filing date
of this application:

_____	_____	_____
(application serial no.)	(filing date)	(status—pending, patented or abandoned)
_____	_____	_____
(application serial no.)	(filing date)	(status—pending, patented or abandoned)

I hereby claim the benefit under Title 35, United States Code, § 119(e) of any United
States provisional application(s) listed below:

_____	_____
(provisional application no.)	(filing date)

I hereby appoint the following Registered Practitioners to prosecute this application and
to transact all business in the Patent and Trademark Office connected therewith:

David V. Trask, Reg. No. 22,012	William S. Britt, Reg. No. 20,969
Laurence B. Bond, Reg. No. 30,549	Joseph A. Walkowski, Reg. No. 28,765
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I hereby declare that all statements made herein of my own knowledge are true and that
all statements made on information and belief are believed to be true; and further that these

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Invention Title: METHOD, CIRCUIT AND SYSTEM FOR DETERMINING BURN-IN RELIABILITY FROM
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statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of first joint inventor: Kenneth W. Marr

Inventor's signature 

Date

7/16/02

Residence: Boise, ID

Citizenship: U.S.A.

Post Office Address: 1361 Hancock Dr., Boise, ID 83706

Document in ProLaw

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Kenneth W. Marr

Serial No.: 10/207,526

Filed: July 29, 2002

For: METHOD, CIRCUIT AND SYSTEM
FOR DETERMINING BURN-IN
RELIABILITY FROM WAFER LEVEL
BURN-IN

Confirmation No.: 9973

Examiner: A. Stevenson

Group Art Unit: 2812

Attorney Docket No.: 2269-5153US
(01-0460.00/US)

CERTIFICATE OF MAILING

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

March 23, 2004
Date

Signature

Joseph A. Walkowski
Name (Type/Print)

ASSOCIATE POWER OF ATTORNEY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. § 1.34(b), please recognize the following individual as an associate agent/attorney herein in connection with the above-identified patent application, and with all continuing and divisional applications hereof:

Jeff M. Michelsen, Reg. No. 50,978

Respectfully submitted,

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